

REFERENCE DESIGNATOR

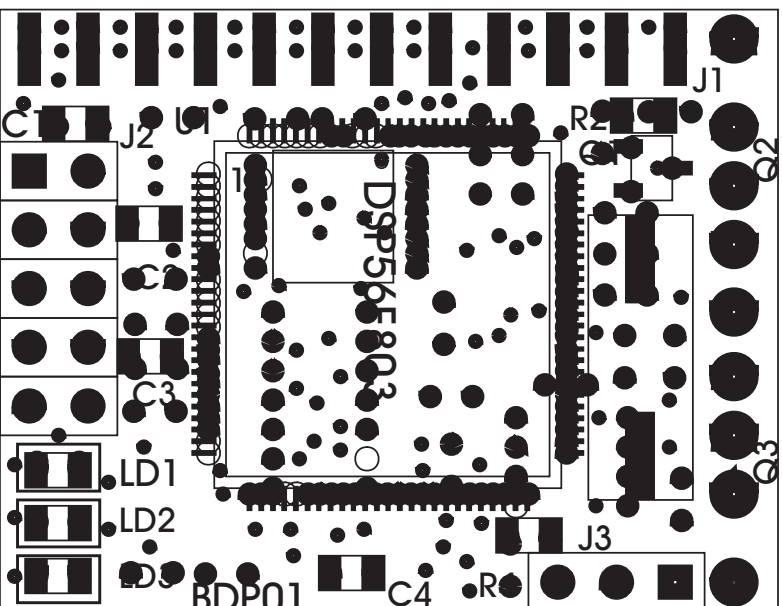
TOP SIDE: CPU

J1 BOTTOM SIDE

J1 TOP SIDE

VIN
GND
RESET
+5V
PWMMA1/ISA1
PWMMA3/FAULT0
PWMMA5/FAULT2
TD2
TA1
TA3
PE5/MOSI
PE7/SS

SOUT
SIN
ATN
GND
PWMMA0/ISAO
PWMMA2/ISAO
PWMMA4/FAULT1
TD1
TA0
TA2
PE4/SCLK
PE6/MISO



MT. HOLE
+5.0V
GND
VIN
GND (MT. HOLE)
+3.3V
GND
+5.0V
MT. HOLE

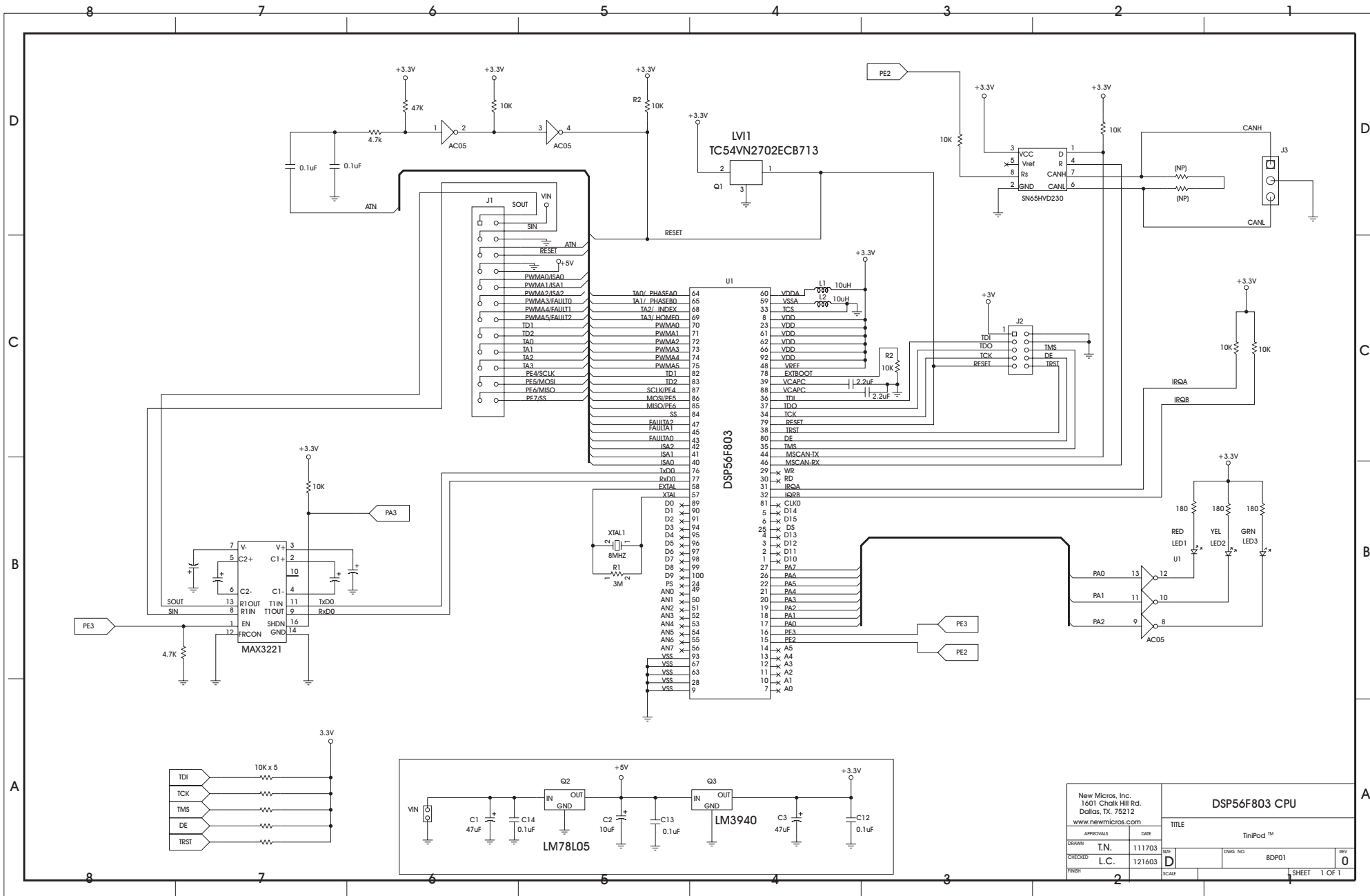
5.0V REGULATOR
3.3V REGULATOR

CANH
GND
CANL

J2
+3.3V
GND
TDI
GND
TDO
TMS
TCK
DE
RESET
TRST

RED LED
YELLOW LED
GREEN LED

PIN 1



New Micros, Inc. 1601 Chokai Hill Rd. Dallas, TX 75212 www.newmicros.com				DSP56F803 CPU TITLE TinPod™			
APPROVALS	DATE	SIZE	DWG. NO.	REV			
DRAWN T.N.	11/17/03	D	8DP01	0			
CHECKED L.C.	12/16/03						
DESIGN		SCALE			SHEET 1 OF 1		

