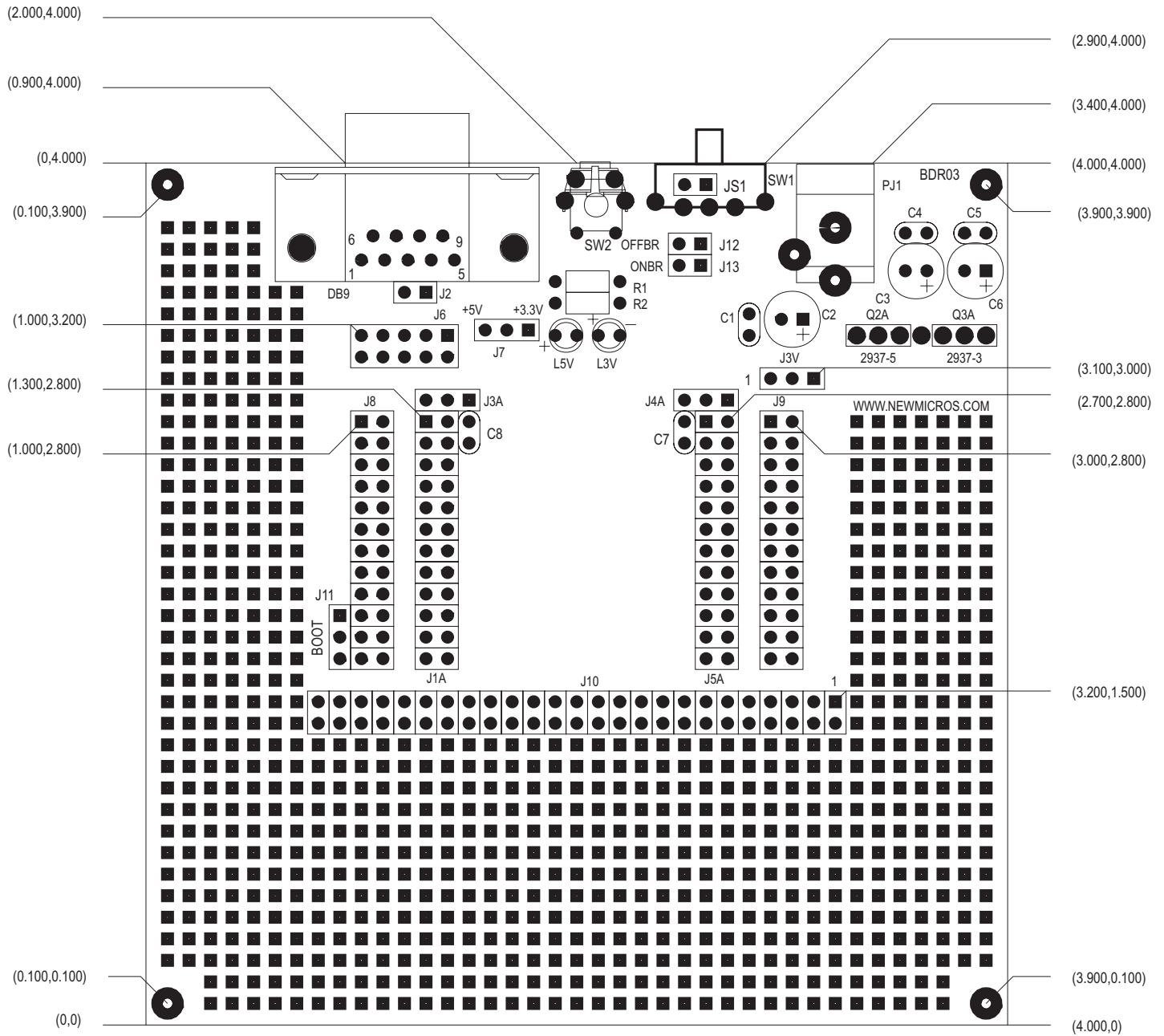
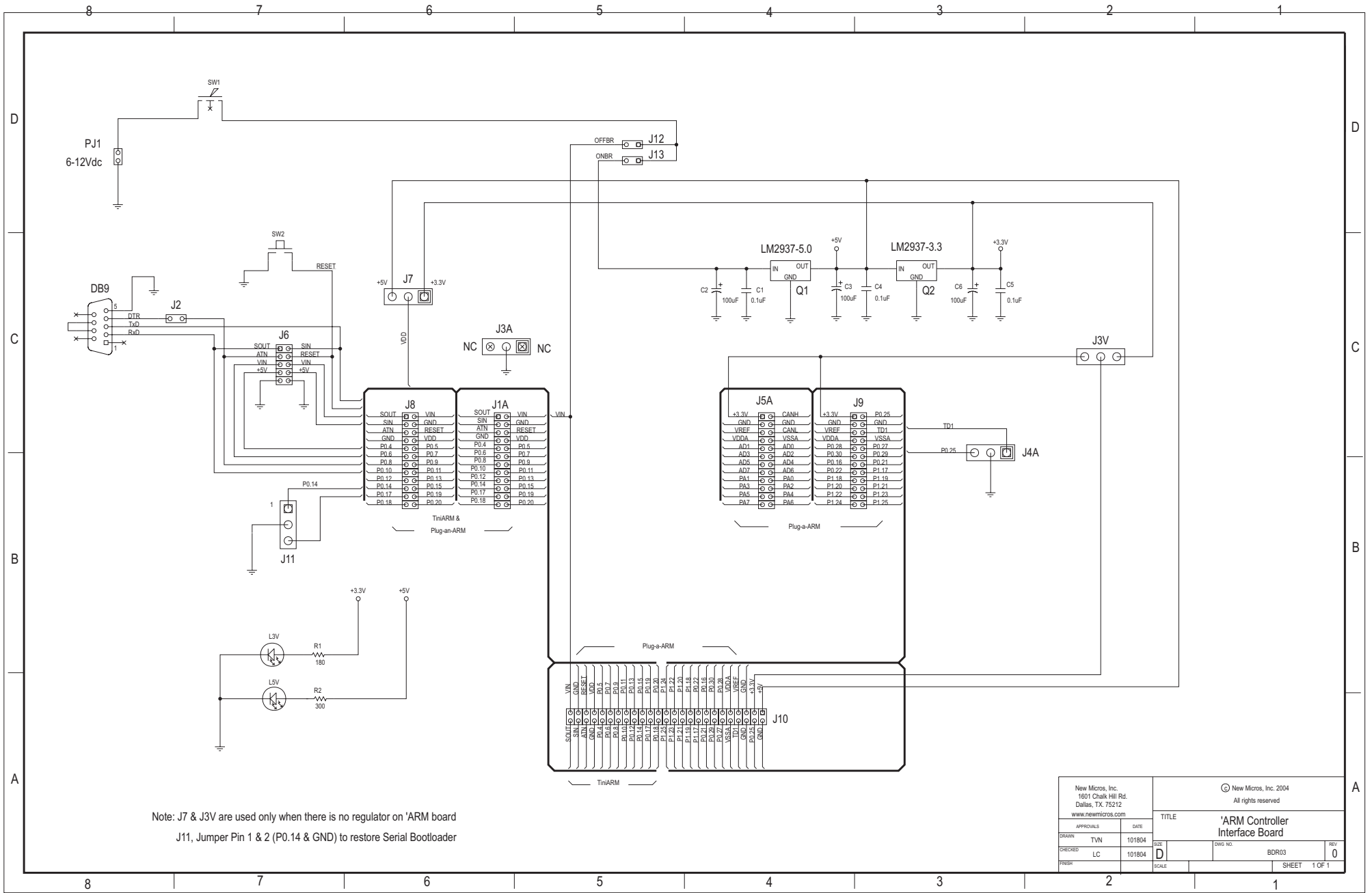


Controller Interface Board





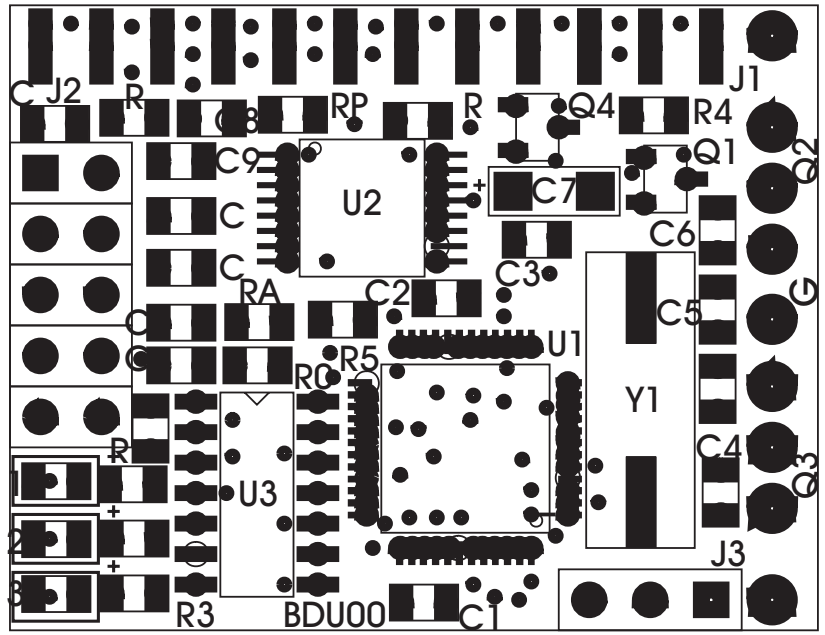
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APPROVALS	DATE	TITLE	
DRAWN TVN	101804	'ARM Controller Interface Board	
CHECKED LC	101804	SIZE D	DWG NO. BDR03
FINISH		SCALE	REV 0
		SHEET 1 OF 1	

J1, REF. DESIGNATOR
CPU: TOP SIDE

J1

TOP		BOTTOM	
1	SOUT	2	VIN
3	SIN	4	GND
5	ATN	6	RESET
7	GND	8	+5V
9	P0.4	10	P0.5
11	P0.6	12	P0.7
13	P0.8	14	P0.9
15	P0.10	16	P0.11
17	P0.12	18	P0.13
19	P0.14	20	P0.15
21	P0.16	22	P0.29
23	P0.30	24	P0.31

+5.0V REG. +3.3V REG.
+5.0V GND +5.0V
GND Vin GND
GND +3.3V GND



P0.3/SDA
GND
P0.2/SCL

J3

J2

Pin 1	+3.3V	GND
	TDI	RTCK
	TDO	TMS
	TCK	DBGSEL
	RESET	TRST

RED LED
YEL LED
GRN LED

