

Plug-A-Mem-256/512 Pinouts

J1

	1	2	VIN
	3	4	GND
	5	6	
	7	8	5.0V(*)
	9	10	
	11	12	
	13	14	
	15	16	
	17	18	CS-FR
WPDS	19	20	CDS
SCLK	21	22	MOSI
MISO	23	24	CS-SD

J5

3.3V(*)	1	2	
GND(*)	3	4	
	5	6	
	7	8	
	9	10	
	11	12	
	13	14	
	15	16	
	17	18	
WP-FR	19	20	
HOLD	21	22	CS-FR(*)
CS-SD(*)	23	24	

Signal descriptions:

VIN - Power input, 5 - 12VDC

GND - Power return, ground

5.0V(*) - 5V input if VIN is not available, optional

3.3V(*) - 3.3V input if both VIN and 5V are not available, optional

CS-FR - Fram Chip select. Active low

WPDS - SD card write protect detection switch

CDS - Card insertion/removal detection switch

SCLK - Serial Clock

MOSI - serial input data, slave

MISO - serial output data, slave

CS-SD - SD card Chip select. Active low

WP-FR - Fram Write Protect. Active low

HOLD - Fram HOLD. Active low.

Optional chip select signals for Fram & SD card can be accessible thru pin #22 and #23 of J5, in case if pin #18 or #24 of J1 is being used by other peripherals. To set up for this configuration, simply remove R7 & R15, and install R8 & R16.

CS-FR(*) - Fram chip select, optional

CS-SD(*) - SD chip select, optional