



NMIS-9003 REAL TIME CLOCK CARD

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The NMIS-9003 Real Time Clock Board, in 2x4" s™ card format, provides a JEDSTACK™ computer system with processor-independent date and time information.

FEATURES

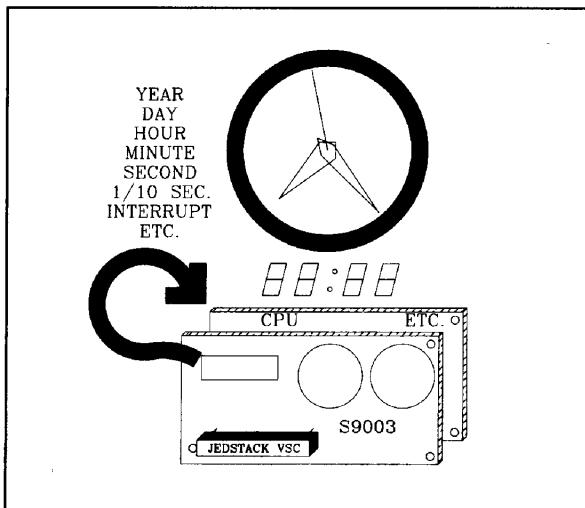
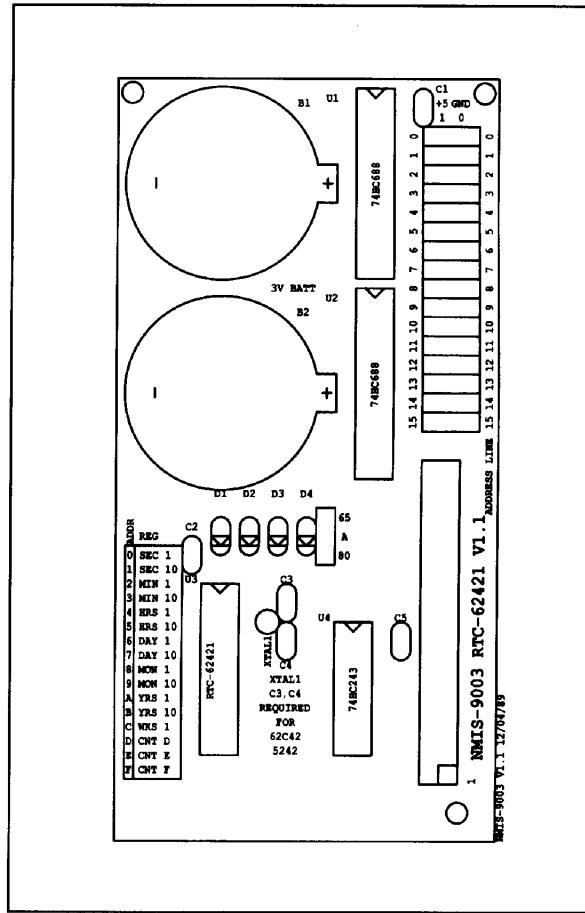
- Built-in Time (hour, minute, second) and Date (year, month, week, day) counters
- 12hr/24hr clock switchover function and automatic leap year setting
- Interrupt masking
- 30 second error-adjustment function
- CMOS design includes low current consumption and backup function

The NMIS-9003 features a built-in quartz oscillator, time and date function, and CMOS circuitry for low power consumption. The EPSON 62421A Real Time Clock chip is the key part on the board. The 62421A's registers are memory mapped by the card. Its registers provide the second, minute, hour, day, month, year, day of week and control information to the processors. The 62421A can operate in either 12 or 24 hour mode. Leap year timing is automatic.

The real time information is maintained and updated through processor power down periods by the on-board, 3V lithium coin battery. Two battery sockets are installed on the board, allowing a fresh battery to be inserted in the open socket before the removal of a failing battery.

The NMIS-9003 can also provide real time interrupts to the processor. This can be useful when very low power modes are desired, in which the processor wakes up only occasionally. The NMIS-9003 can create such an interrupt once an hour, once a minute, once a second, or 64 times a second.

A Vertical Stacking Connector in the lower right hand corner (top view) provides connections to the processor's address and data bus, control signals, 5V power and ground. Address decoding of the Real Time Clock chip's space in memory is accomplished by two octal comparators and 16 two-position jumpers. The NMIS-9003 occupies 16 addresses. Any 16-byte boundary in the 64K address space of the JEDSTACK™ processor's bus can be selected by correct jumper placement.



Application

NMIS-9003

REAL TIME CLOCK

2x4" s

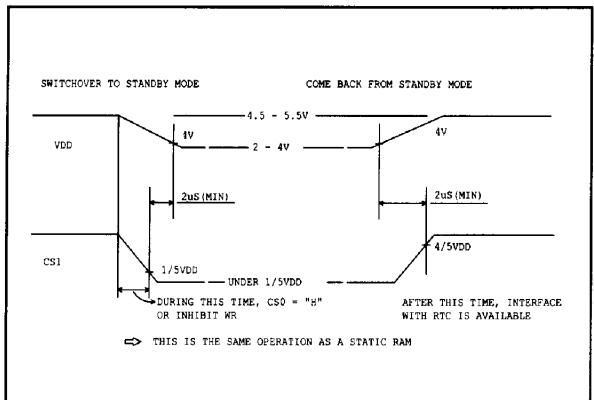
NMIS-9003

Item	Conditions		MAX. Value	Unit
Frequency Tolerance	Ta = 25°C		± 10	ppm
	V _{DD} = 5V		± 50	
Frequency Temp. Characteristics	(-10~+70°C at 25°C = 0 ppm)		+10 ~ 120	ppm
Aging	V _{DD} = 5V Ta = 25°C First year		± 5	ppm year

Frequency Characteristics

Item	Symbol	Conditions	Value	Unit
Power Supply	V _{DD}		4.5 ~ 5.5	V
Data-holding	V _{DH}		2.0 ~ 5.5	V
Crystal Frequency	F ₀	Ta = 25°C	32.768	kHz
Operating Temp.	T _{OPR}		-10 ~ +70	°C

Operating Range



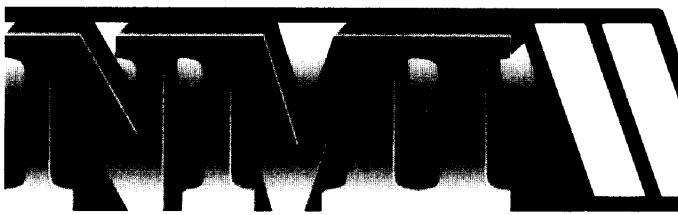
Address	A ₃	A ₂	A ₁	A ₀	Register	Data				Count Value	Remarks
						D3	D2	D1	D0		
0 0 0 0 0	S ₁	S ₈	S ₄	S ₂	S ₁	0 ~ 9	1-second digit register				
1 0 0 0 1	S ₁₀	*	S ₄₀	S ₂₀	S ₁₀	0 ~ 5	10-second digit register				
2 0 0 1 0	M ₁	m ₈	m ₄	m ₂	m ₁	0 ~ 9	1-minute digit register				
3 0 0 1 1	M ₁₀	*	m ₄₀	m ₂₀	m ₁₀	0 ~ 5	10-minute digit register				
4 0 1 0 0	H ₁	h ₈	h ₄	h ₂	h ₁	0 ~ 9	1-hour digit register				
5 0 1 0 1	H ₁₀	*	PM/A M	h ₂₀	h ₁₀	0 ~ 2	PM/AM, or 0 ~ 1	10-hour digit register			
6 0 1 1 0	D ₁	d ₈	d ₄	d ₂	d ₁	0 ~ 9	1-day digit register				
7 0 1 1 1	D ₁₀	*	*	d ₂₀	d ₁₀	0 ~ 3	10-day digit register				
8 1 0 0 0	M _{O1}	m ₀₈	m ₀₄	m ₀₂	m ₀₁	0 ~ 9	1-month digit register				
9 1 0 0 1	M _{O10}	*	*	*	m ₀₁₀	0 ~ 1	10-month digit register				
A 1 0 1 0	Y ₁	y ₈	y ₄	y ₂	y ₁	0 ~ 9	1-year digit register				
B 1 0 1 1	Y ₁₀	y ₈₀	y ₄₀	y ₂₀	y ₁₀	0 ~ 9	10-year digit register				
C 1 1 0 0	W	*	w ₄	w ₂	w ₁	0 ~ 6	Week register				
D 1 1 0 1	Reg D	30 sec ADJ	IRQ FLA+G	BUSY	HOLD	—	Control Register D				
E 1 1 1 0	Reg E	t ₁	t ₀	ITRPT /STND	MASK	—	Control Register E				
F 1 1 1 1	Reg F	TEST	24/12	STOP	REST	—	Control Register F				

FUNCTION TABLE

STANDBY Mode

WORLD HEADQUARTERS WORLDWIDE REPRESENTATIVES

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